

Application No.: 10/698,805

Docket No.: MWS-058

AMENDMENTS TO THE CLAIMSRECEIVED  
CENTRAL FAX CENTER

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1. (currently amended) In an electronic device, a method comprising the steps of:  
grouping a first data signal of a first signal type and a second data signal of a second signal type to form a bus signal in a graphical model displayed on a graphical user interface;  
providing the bus signal as input to a non-virtual operation block; and  
performing an non-virtual operation on the bus signal with the non-virtual operation block.
2. (currently amended) The method of claim 1, ~~wherein the graphical model is a block diagram, and the step of performing a non-virtual operation on the bus signal comprises constructing the block diagram including an operation block representing the non-virtual operation and connecting a representation of the bus signal in the block diagram to an input port of the operation block wherein at least a third data signal is grouped with the first data signal and the second data signal to form a bus signal.~~
3. (currently amended) The method of claim 2, wherein an outlet of the non-virtual operation block connects to a modified bus signal comprising a modified first data signal, where the modified first data signal represents an output of the non-virtual operation where the first data signal is an input to the non-virtual operation, and a modified second data signal, where the modified second data signal represents an output of the non-virtual operation where the first data signal is an input to the non-virtual operation.
4. (currently amended) The method of claim 2, wherein the step of performing an non-virtual operation represented by the non-virtual operation block comprises solving the non-virtual operation using values represented by the first data signal and the second data signal as inputs to the non-virtual operation.
5. (currently amended) The method of claim 4, where the step of solving the non-virtual operation comprises the steps of converting the graphical model to executable computer readable instructions representing the graphical model and executing the computer readable instructions, wherein the computer readable instructions implement the functionality specified by the model.

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6. (original) The method of claim 1, further comprising the step of defining one or more physical attributes for the first data signal and the second data signal of the bus signal.

7. (currently amended) The method of claim 2, wherein the bus signal has a structure that is the same at an output port of the non-virtual operation block as at an input port of the non-virtual operation block.

8. (currently amended) The method of claim 1, wherein the non-virtual operation comprises one of multiplication, division, integration, derivation, a linear transfer function, a delay function, a transfer function specified in terms of poles and zeros, a dead zone function, a switching function, a quantizing function and a rate limiting function on values represented by the first data signal and the second data signal.

9. (original) The method of claim 1, wherein the first signal type and the second signal type are different.

10. (original) The method of claim 1, further comprising the step of validating a constraint on the bus signal.

11. (withdrawn) In an electronic device, a method comprising the steps of:  
providing a definition for a bus signal; and  
combining a first data signal with a second data signal in a graphical model displayed on a graphical user interface to form the bus signal having a structure defined by the definition.

12. (withdrawn) The method of claim 11, further comprising the step of generating computer readable instructions corresponding to the definition of the bus signal.

13. (withdrawn) The method of claim 11, wherein the step of providing a definition to the bus signal comprises entering a physical attribute for one of said first data signal and said second data signal via a graphical user interface.

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14. (withdrawn) The method of claim 11, wherein the step of providing a definition comprises defining a complexity of one of said first data signal and said second data signal.

15. (withdrawn) The method of claim 11, wherein the step of providing a definition comprises defining a data type for an attribute of one of said first data signal and said second data signal.

16. (withdrawn) The method of claim 11, wherein the step of providing a definition comprises defining a dimension of one of said first data signal and said second data signal.

17. (withdrawn) The method of claim 11, wherein the step of providing a definition comprises defining a size of one of said first data signal and said second data signal.

18. (withdrawn) The method of claim 11, wherein the step of providing a definition comprises defining a sample rate for the bus signal.

19. (withdrawn) The method of claim 11, wherein the step of defining provides a complete definition for the bus signal.

20. (withdrawn) The method of claim 11, wherein the step of defining provides a partial definition for the bus signal.

21. (withdrawn) The method of claim 11, further comprising the step of specifying an implementation of the bus signal in computer readable instructions.

22. (withdrawn) The method of claim 21, wherein the bus signal is represented in the computer readable instructions as one of: a structure passed by reference, a global structure referenced statically as global, a representation wherein element of the bus signal is an individual piece of unstructured data and a representation wherein individual bus elements are passed as formal parameters.

23. (withdrawn) The method of claim 11, further comprising the steps of automatically determining a representation of the bus signal in computer executable instructions corresponding

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to a graphical model implementing the bus signal and implementing the bus signal in the computer readable instructions in the determined representation.

24. (withdrawn) The method of claim 11, further comprising the step of connecting the bus signal in the graphical model to a non-virtual block to represent a mathematical operation to be performed on values represented by the first data signal and the second data signal.

25. (withdrawn) The method of claim 24, wherein the non-virtual block preserves the structure of the bus signal across the non-virtual block.

26. (withdrawn) In an electronic device, a method comprising the steps of:  
entering a definition for a bus signal in a graphical user interface; and  
saving the definition for the bus signal in memory.

27. (withdrawn) The method of claim 26, further comprising the step of constructing a graphical model representing a dynamic system that implements the bus signal.

28. (withdrawn) The method of claim 27, wherein the definition is associated with the graphical model in memory.

29. (withdrawn) The method of claim 27, wherein the definition of the bus signal is saved independently of the graphical model.

30. (withdrawn) In an electronic device, a method comprising the steps of:  
defining a definition for a bus signal used in a graphical model displayed on a graphical user interface; and  
generating computer readable instructions corresponding to the bus signal.

31. (withdrawn) In an electronic device, a method comprising the steps of:  
combining a first data signal and a second data signal together to form a bus signal in a graphical model displayed on a graphical user interface; and  
passing the bus signal from a first domain to a second domain in the graphical model.

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32. (currently amended) In an electronic device, a medium holding computer-executable instructions for a method, comprising the steps of:

grouping a first data signal of a first signal type and a second data signal of a second signal type to form a bus signal in a graphical model displayed on a graphical user interface;  
providing the bus signal as input to a non-virtual operation block; and  
performing an non-virtual operation on the bus signal with the non-virtual operation block.

33. (currently amended) The medium of claim 32, wherein at least a third data signal is grouped with the first data signal and the second data signal to form a bus signal,  
wherein the graphical model is a block diagram, and the step of performing a non-virtual operation on the bus signal comprises:

~~— a user constructing the block diagram including an operation block representing the non-virtual operation a graphical user interface and connecting a representation of the bus signal in the block diagram to an input port of the operation block.~~

34. (currently amended) The medium of claim 33, wherein the step of performing an non-virtual operation represented by the non-virtual operation block comprises solving the non-virtual operation using values represented by the first data signal and the second data signal as inputs to the non-virtual operation.

35. (currently amended) The medium of claim 34, where the step of solving the non-virtual operation comprises the steps of:

converting the graphical model to executable computer readable instructions representing the graphical model; and  
executing the computer readable instructions, wherein the computer readable instructions implement the functionality specified by the model.

36. (original) The medium of claim 32, further comprising the step of defining a definition for the bus signal.

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37. (original) The medium of claim 32, further comprising the step of validating a constraint on the bus signal.

38. (withdrawn) In an electronic device, a medium holding computer-executable instructions for a method, comprising the steps of:

    a user entering a definition for a bus signal; and

    combining a first data signal with a second data signal in a graphical model displayed on a graphical user interface to form the bus signal having a structure defined by the definition.

39. (withdrawn) The medium of claim 38, further comprising the step of generating computer readable instructions corresponding to the definition of the bus signal.

40. (withdrawn) The medium of claim 38, further comprising the steps of determining a representation of the bus signal in computer executable instructions corresponding to a graphical model implementing the bus signal and implementing the bus signal in the computer readable instructions in the representation.

41. (withdrawn) In an electronic device, a medium holding computer-executable instructions for a method, comprising the steps of:

    a user entering a definition for a bus signal in a graphical user interface; and

    saving the definition for the bus signal in memory.

42. (currently amended) A system for generating and displaying a modeling application for simulating a dynamic system, comprising:

    user-operable input means for inputting data to the application;

    a display device for displaying a graphical model representing the dynamic system; and

    an electronic device including memory for storing computer program instructions and data, and a processor for executing the stored computer program instructions, the computer program instructions including instructions for performing an an non-virtual operation on a bus signal displayed in the graphical model, wherein the bus signal comprises a first data signal of a first signal type and a second data signal of a second signal type grouped together to form the bus signal.

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43. (withdrawn) A system for generating and displaying a modeling application for simulating a dynamic system, comprising:

user-operable input means for inputting data to the application;  
a display device for displaying a graphical model representing the dynamic system; and  
an electronic device including memory for storing computer program instructions and data, and a processor for executing the stored computer program instructions, the computer program instructions including instructions forming a bus signal having a structure defined by a definition entered by the user using the user-operable input means, by combining a first data signal with a second data signal in the graphical model displayed on a graphical user interface to form the bus signal.

44. (withdrawn) A system for generating and displaying a modeling application for simulating a dynamic system, comprising:

user-operable input means for inputting data to the application;  
a display device for displaying a graphical model representing the dynamic system; and  
an electronic device including memory for storing computer program instructions and data, and a processor for executing the stored computer program instructions, the computer program instructions including instructions receiving a definition for a bus signal from a user via the user-operable input means, and saving the definition for the bus signal in memory.